

15437-0579

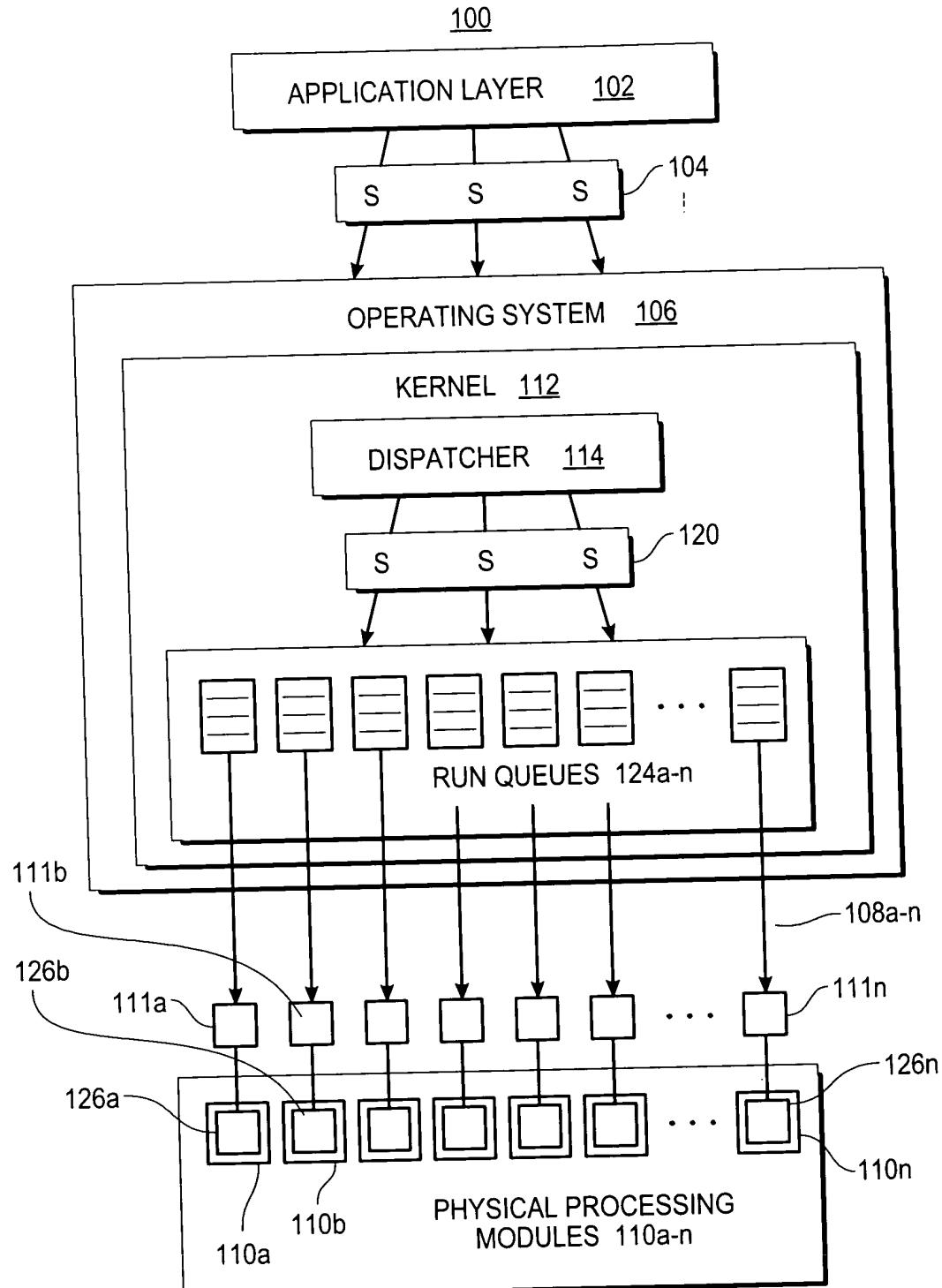


Fig. 1A

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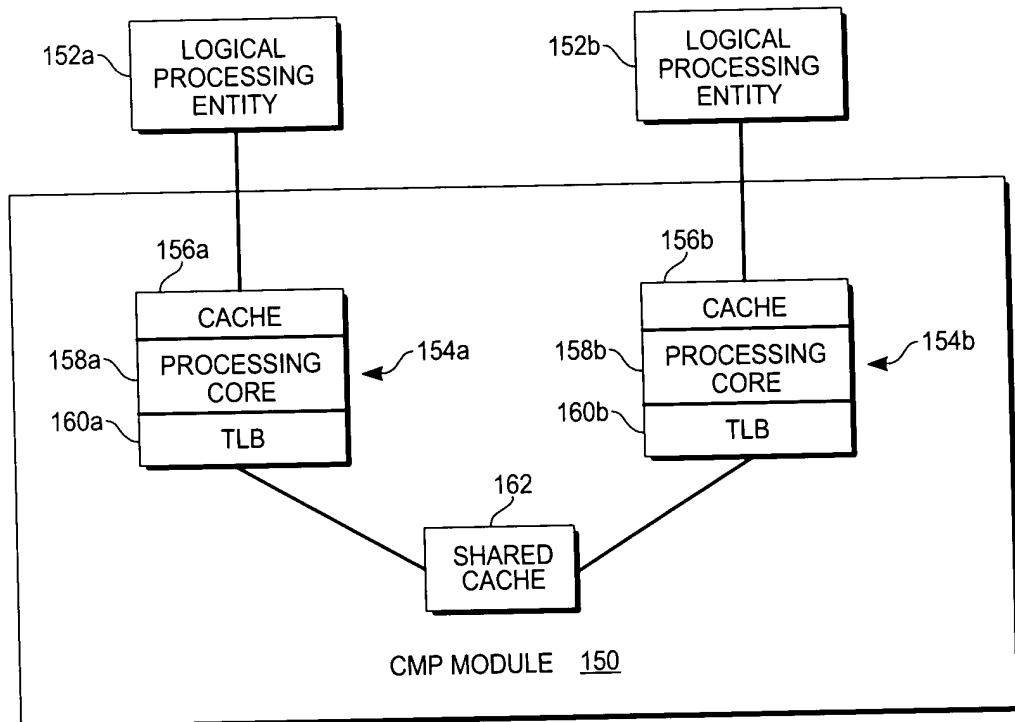


Fig. 1B

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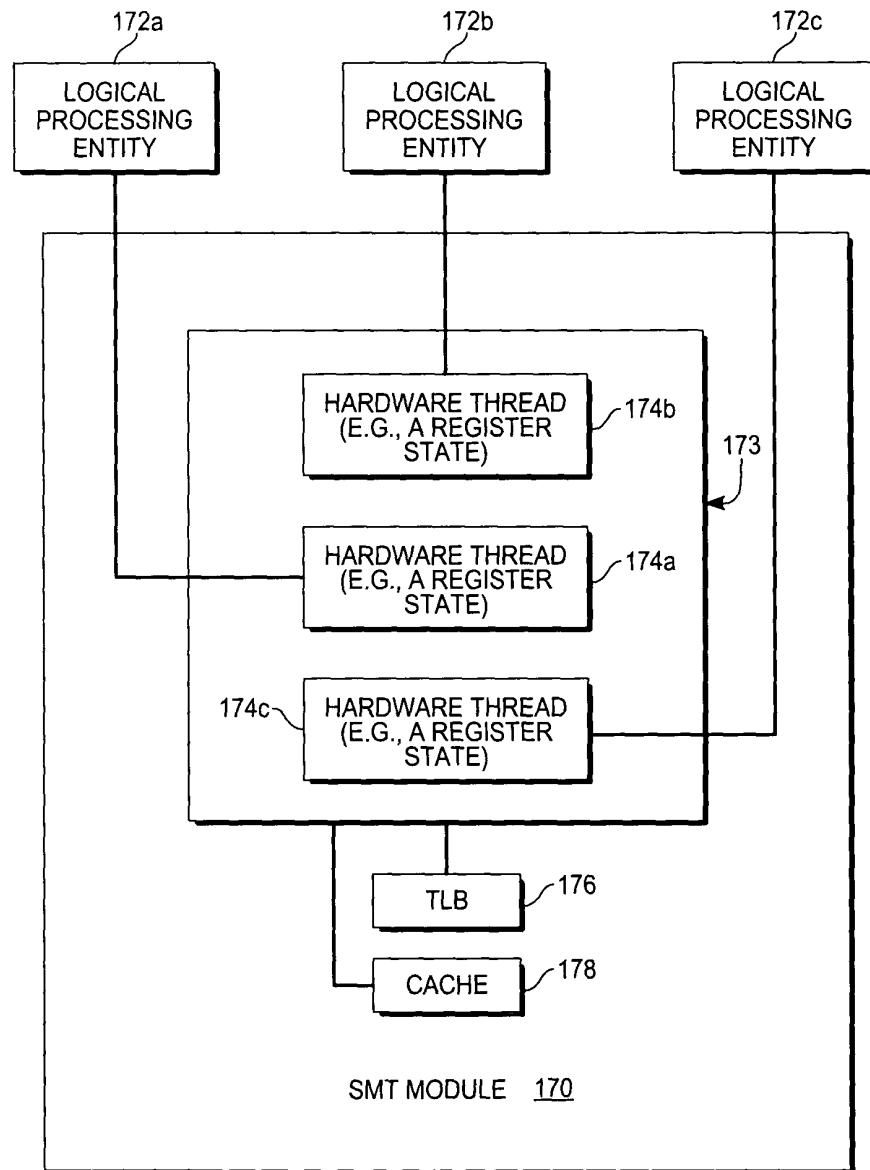


Fig. 1C

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Title: PROCESSOR SPECIFIC DISPATCHING IN A
HETEROGENEOUS CONFIGURATION

Inventor(s): Eric C. Saxe, et al.

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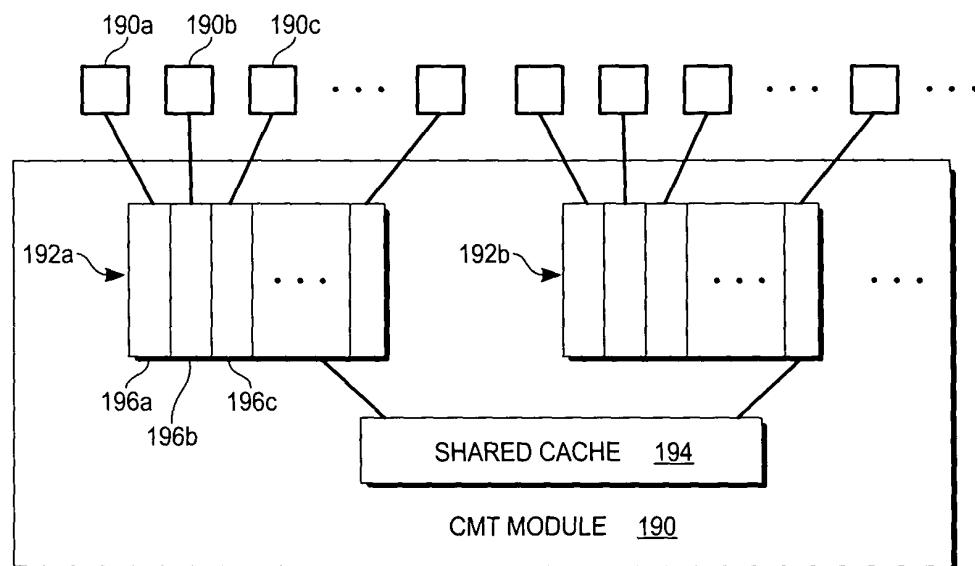


Fig. 1D

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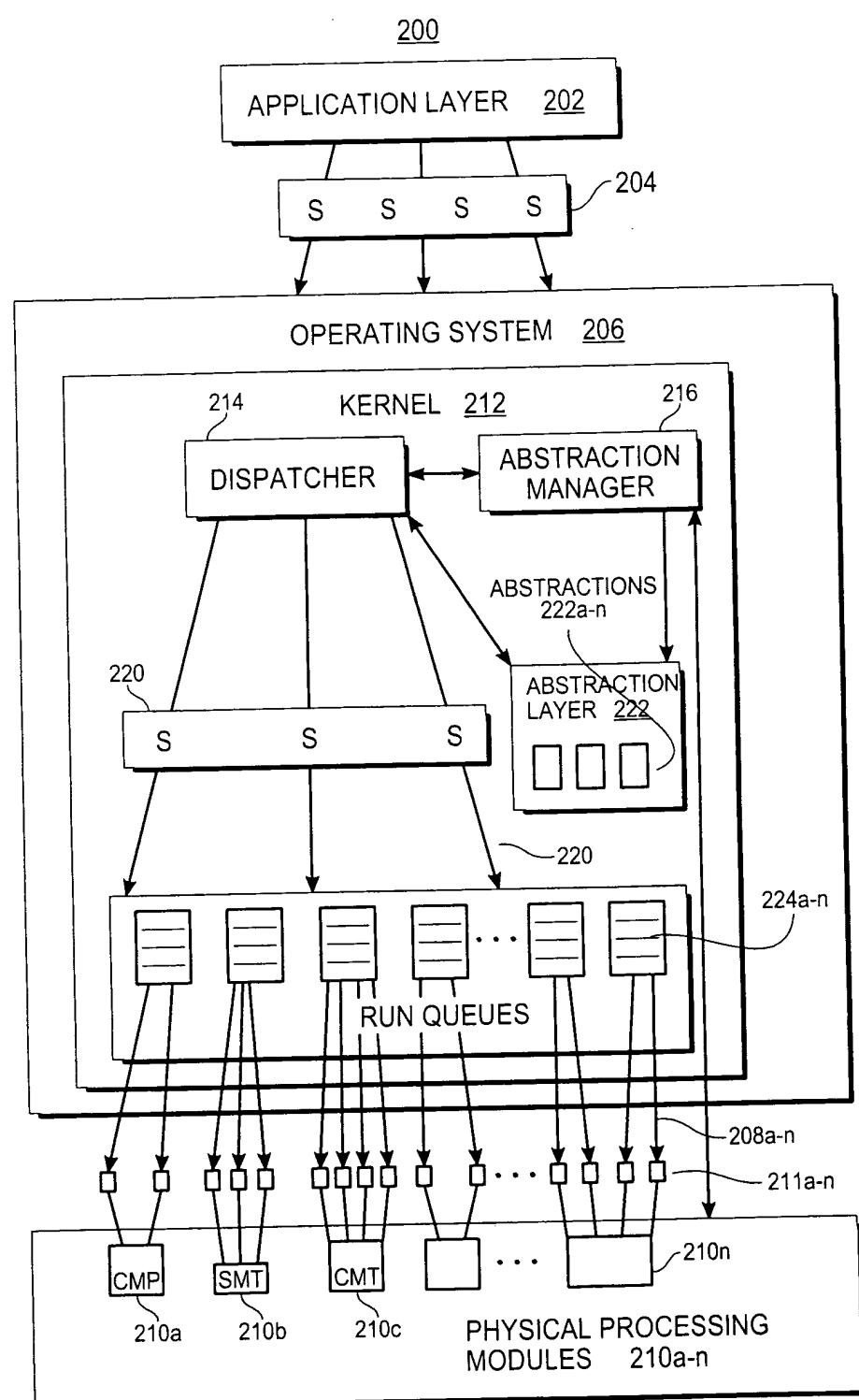


Fig. 2A

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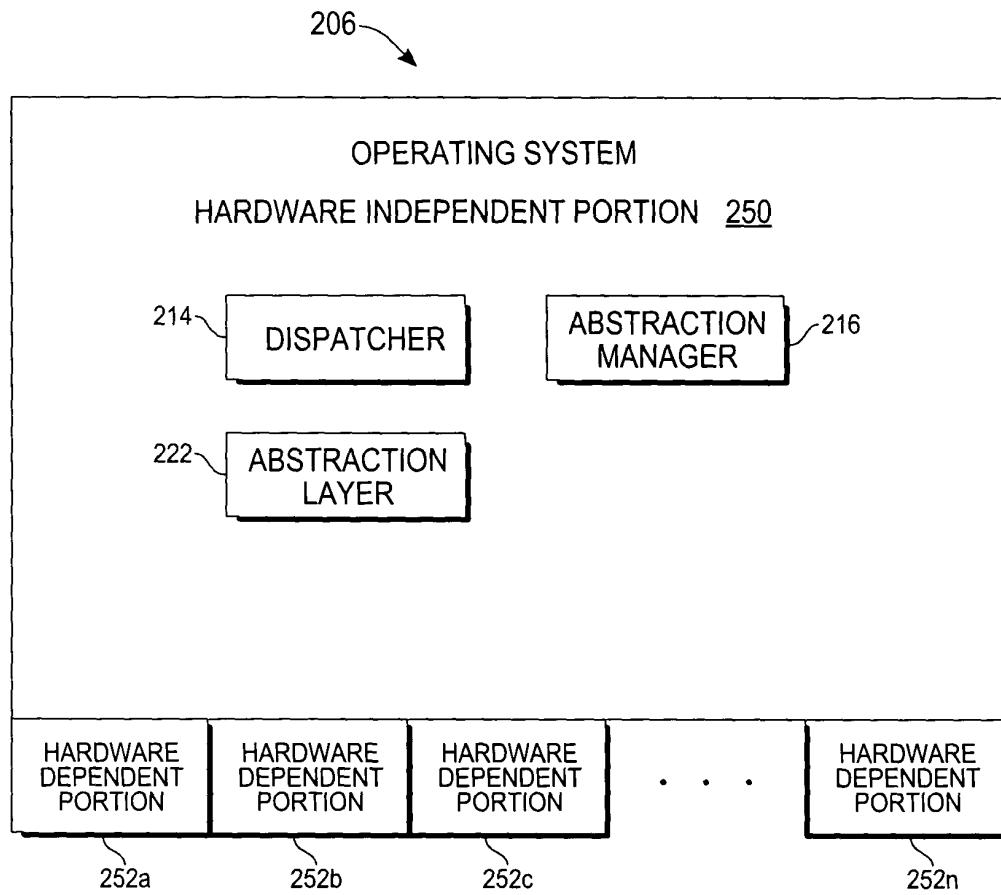
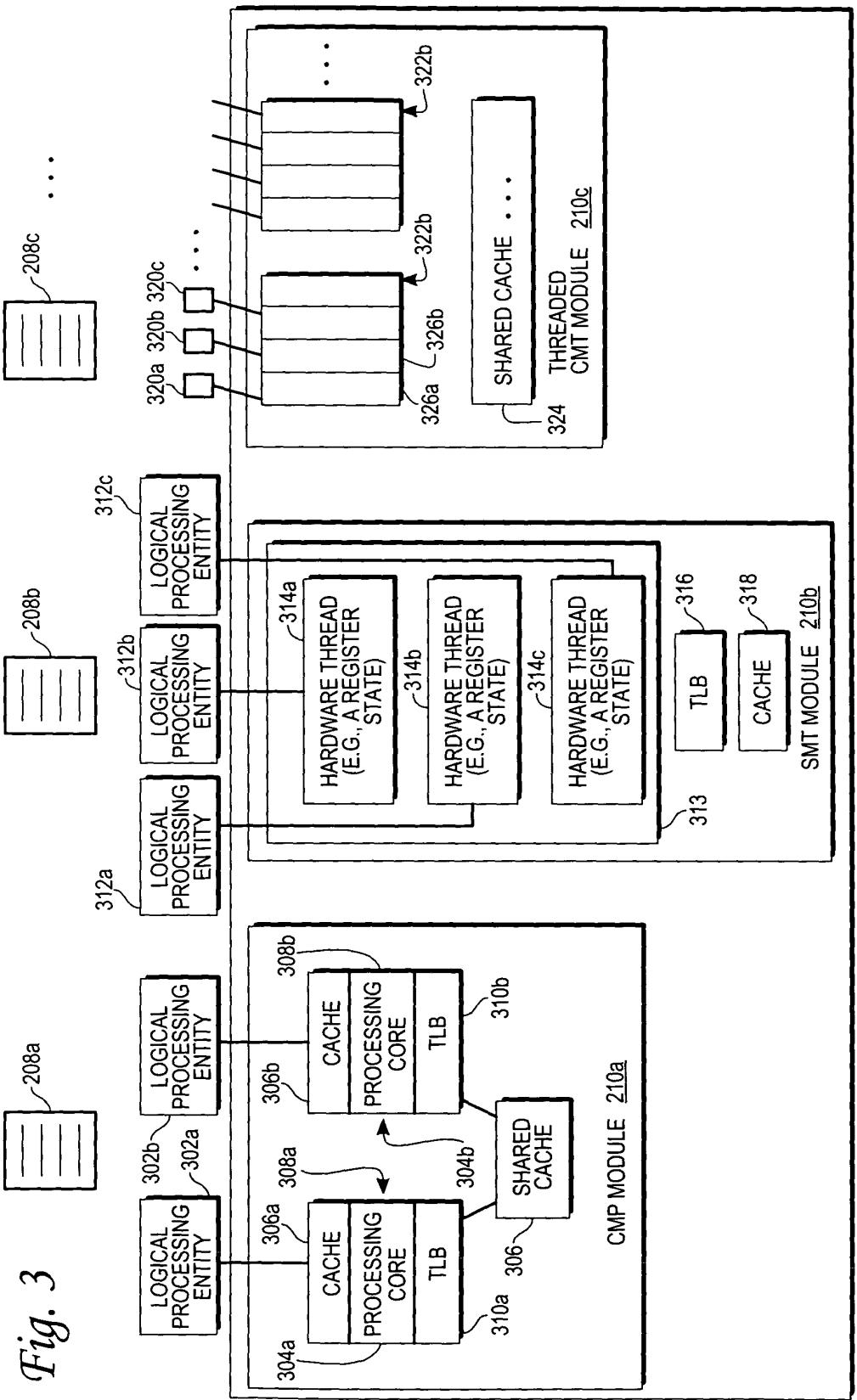


Fig. 2B

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ABSTRACTION 400

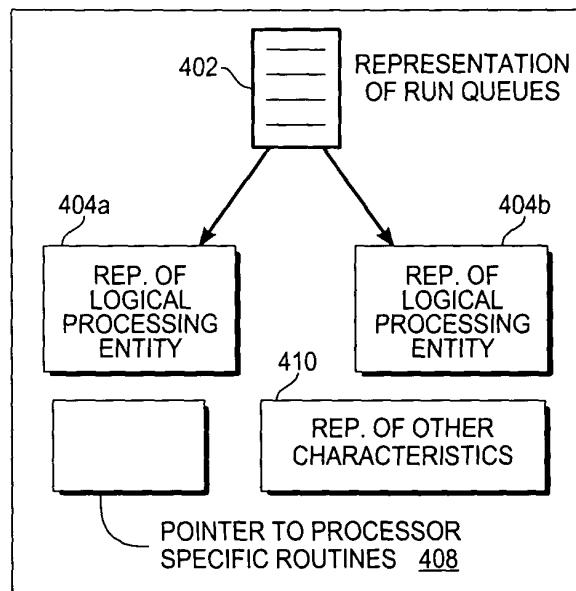


Fig. 4

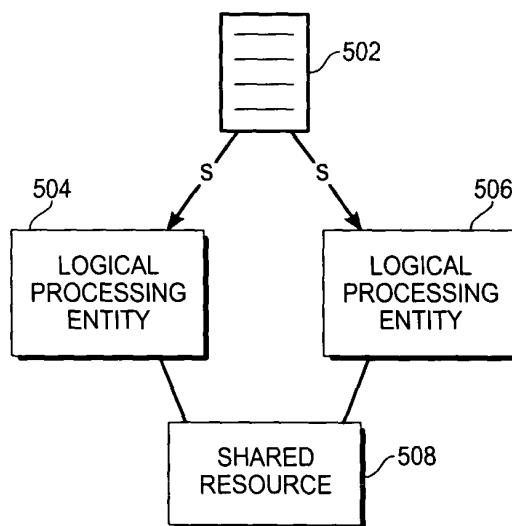


Fig. 5

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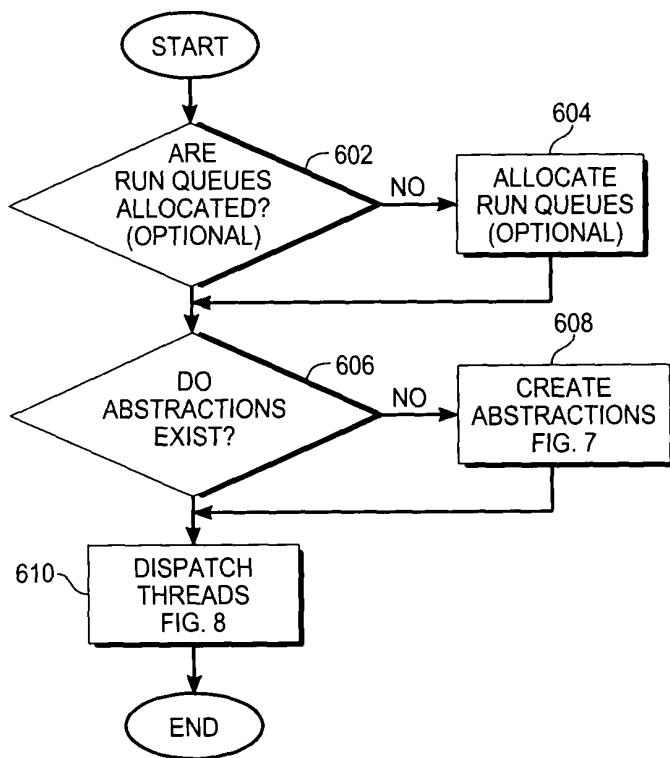


Fig. 6

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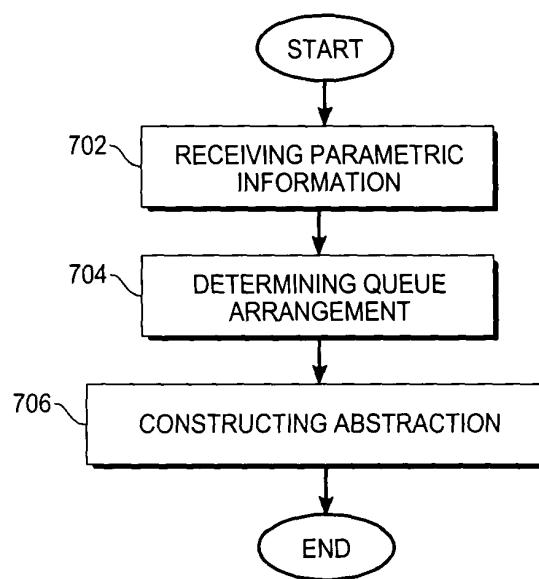


Fig. 7

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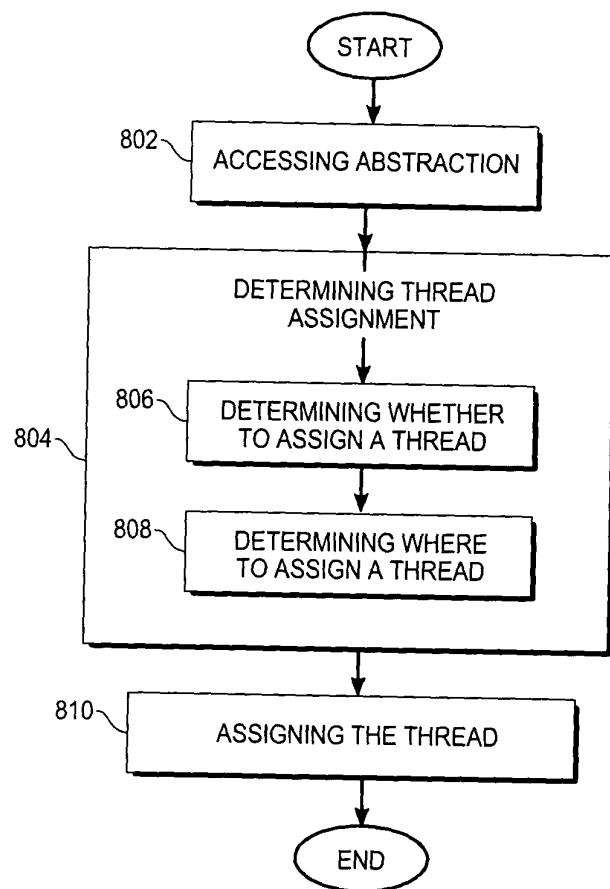


Fig. 8

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